

PTO/SB/08A (08-03)

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

**Complete if Known**

Application Number	10/714,745
Filing Date	11/17/2003
First Named Inventor	Brian J. Campbell
Art Unit	2819
Examiner Name	Tan, Vibol
Attorney Docket Number	BP2243CON

Sheet 1 of 2

**U. S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
tan		US- 5,459,736	10/17/95	Nakamura	
tan		US- 5,689,517	11/18/97	Ruparel	
tan		US- 4,061,933	12/6/77	Schroeder et al.	
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tan		US- 5,870,411	2/9/99	Durham et al.	
		US-			
		US-			

**FOREIGN PATENT DOCUMENTS**

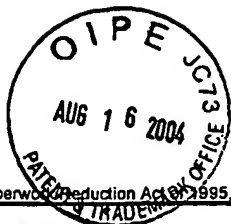
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T <sup>3</sup>
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				
tan		522 413	1/13/93	EP		

Examiner Signature		Date Considered	8/26/04
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Art Unit 2819

Examiner Name Tan, Vibol

Sheet

2

of

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Attorney Docket Number BP2243CON

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
tan		Stephany et al., "FP15.5: A 200 MHz 32b 0.5W CMOS RISC Microprocessor," Digital Semiconductor, Austin TX, IEEE, 1998, pp. 15.5-1 to 15.5-9.	
tan		Santhanam et al., "SA 18.6: A Low-Cost 300MHz RISC CPU with Attached Media Processor," Digital Equipment Corp., Palo Alto, CA, IEEE, 1998, pp. 18.6-1 to 18.6-9.	
tan		Montanaro et al., "A 160-MHz, 32-b, 0.5-W CMOS RISC Microprocessor," IEEE Journal of Solid-State Circuits, vol. 31, No. 11, Nov. 1996, 12 pages.	
tan		Weiss et al., "The On-Chip 3MB Subarray Based 3rd Level Cache on an Itanium Microprocessor," IEEE, ISSCC 2002, Session 6, 3 pages.	
tan		Josephson et al., "Test Methodology for the McKinley Processor," IEEE, ITC International Test Conference, Paper 21.1., 2001, pp. 578-585.	
tan		Santhanam et al., Presentation for: "A 1GHz Power Efficient Single Chip Multiprocessor System For Broadband Networking Applications," Broadcom Corp., Jun. 14, 2001, 19 pages.	
tan		Santhanam et al., "A 1GHz Power Efficient Single Chip Multiprocessor System For Broadband Networking Applications," Broadcom Corp., Jun. 14, 2001, 4 pages.	

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